Linux on the Cell processor Linux Kernel Hacking Free Course — IV edition

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What is Cell?



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 Cell is a multiprocessor system on single chip developed by IBM in collaboration with Sony and Toshiba





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- Eight special purpose processors (SPE)



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- Cell has a non homogeneous architecture
- One general purpose processor (PPE)
- Eight special purpose processors (SPE)
- To fully exploit the Cell architecture a new programming approach is required



Architectural Overview





The Power Processor Element:

 The main processor: it executes both the operating system and the general purpose applications, and it spawns tasks to SPE



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- A dual-threaded general purpose processor
- Based on a 64 bit RISC architecture conforming to the PowerPC Architecture version 2.02
- Has vector/SIMD multimedia extensions



PPE simple block diagram

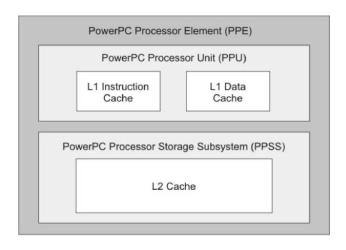




Image taken from CBE Programming Tutorial v. 3

Synergistic Processor Element (SPE)

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Each SPE is:

- Slave processor: it execute tasks spawned from the PPE
- Based on a 128 bit RISC architecture specialized for computing intensive SIMD applications



SPE simple block diagram

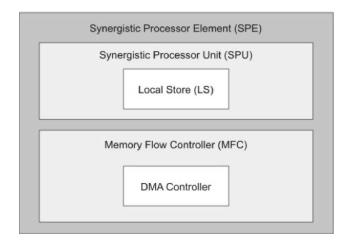




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Deals with instructions execution and control



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- New SIMD (Single Instruction Multiple Data) instruction set



Local Store (LS)



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- The SPU fetches instructions and load/store data from/to its own Local Store





 It's the interface between the SPE and the other system processors



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- Contains a DMA controller for DMA transfers support



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It's the interface between the SPF and the other

 After a DMA command has been queued, the SPU can continue to execute instructions while the MFC processes the DMA command

DMA tranfers



DMA tranfers

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- The SPU associated with MFC can issue a DMA-list of up to 2048 DMA



High level programming





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- PPE program a program running on PowerPC core that offloads task to SPE
- SPE program a program running on SPE processor that uses the SPU Instruction Set





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- spe_program_load() load an SPE program into the context
- spe_context_run() execute a context on a physical SPE

Creating a SPE thread from PPE and libspe2

 The function aboves are in libspe2, which is an implementation of the SPE Runtime Management Library developed by IBM under GPL license and downlodable from http://sourceforge.net/projects/libspe





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- This program can use SPE (vectorial) data types and SIMD instructions
- SIMD instructions are defined in the $SPU\ C/C++$ language extensions and are named intrinsics
- A SPE program transfers data from/to main memory to/from Local Store through DMA transfers





Use vector data type instead of scalars



- Use vector data type instead of scalars
- Perform loop unrolling



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- Perform loop unrolling
- Use double buffering





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- For example a scalar load must be rotated into the preferred slot



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- Loop unrolling increases the size of code
- Data and code must fit in 256 KB Local Store



Double buffering (1/2)



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- The communication bus between SPE's and PPE is a bottleneck
- In the Cell architecture DMA transfers are asynchronous
- This feature allow the programmer to schedule the transfers so that the latency of memory accesses can be hidden by overlapping the transfers in one buffer with computations in another

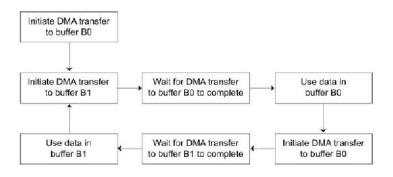


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Cell and the Linux Kernel





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- The second folder include code for supporting the Cell on Sony PlayStation 3

Differences between native Cell and Cell on ps3



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Differences between native Cell and Cell on ps3

- In native Cell the Linux kernel runs directly on hardware
- In ps3 the Linux kernel runs in a virtualized environment



Why different platforms?



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 Why there are different platforms for Cell native and Cell on ps3?



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- Why there are different platforms for Cell native and Cell on ps3?
- The presence of a virtualization layer imposes different low level interactions between hardware devices and kernel



Kernel execution overview on native Cell

video SPU Platform USB graphics audio network storage output support support control Х libspe utils Optical TCP/IP Mouse/KBD PAD PPC64 infrastructure USB mass Bluetooth etc Video 왏 PS3 Core support ALSA FΒ Output SPE Control SCSI support NETWORK VFB GbE USB audio storage System Bus GbE ATA USB Video SPUs PPU Output Graphics Audio HDD/ Control K.BO WiFi BD Bluetooth

function

Linux kernel

ΜW



Kernel execution overview on PlayStation3

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Linux kernel

Hyper visor

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 $Image\ taken\ from\ http://www.kernel.org/pub/linux/kernel/people/geoff/cell/$

How libspe2 create a SPE context - spu_create() syscall



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- The spu_create() system call creates a spu context in kernel memory and return an open file descriptor for the directory (in /spu) associated with it.



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- By convention mounted in /spu
- Directories in /spu represent SPE contexts whose properties are shown as regular files
- Interaction with these contexts can happen through file operations like open, read, write, etc.



Examples of files in a spufs sub-directory



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 mem – The local memory of a SPE context. Mainly used to load the executable file of the program to be run onto the SPE



Examples of files in a spufs sub-directory

- mem The local memory of a SPE context. Mainly used to load the executable file of the program to be run onto the SPE
- regs The general purpose registers of an SPE.
 Normally can't be accessed directly but they can be saved in a context in kernel memory





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- The kernel can use this structure to save the state of a SPE thread
- Context switching on SPE is very inefficient





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- Instead, it makes use of a file memory mapping of the mem file
- Thus, the SPE ELF object file is loaded into the context directly from user space



Running a SPE program – spu_run() syscall



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- Each SPE thread is associated with one PPE thread





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- With ps3 Cell is incredibly low cost
- The University of Massachusetts Dartmouth uses a cluster of sixteen ps3 for astrophysics analysis



Performance scaling with implementation

2048x2048 float matrix multiplication on single SPE

Implementation	Execution time (ms)
Scalar	338687.230514
Vectorial	336059.746404 (-0,77%)
Vectorial - Unrolling	280815.662356 (-17%)
Vectorial - Unrolling	262594.693659 (-23%)
con spu_madd	
Vectorial - spu_madd	75076.210915 (-78%)
Vectorial - spu_madd -	18072.911028 (-95%)
spu_gcc -O3	
Vectorial - spu_madd -	10509.868133 (-97%)
with Double Buffering	





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- Each SPE is capable of 25.6 GFLOPS in integer and single precision arithmetic
- Fully exploiting Cell capabilities is not easy

